

Declaration

I hereby declare that the information furnished above is true to the best of my knowledge. I agree to abide by the rules and regulations governing the programme. If selected, I shall attend the course for the entire duration. I also undertake the responsibility to inform the coordinators sufficiently in advance, in case I am unable to attend the course.

Place:

Date: Signature of the applicant

Sponsorship Certificate

Certified that Mr./ Ms./ Dr.

.....
is an employee of this institution and is hereby sponsored for the FDP **Advanced Digital Signal Processing Algorithms and Architectures** at **National Institute of Technology Calicut** during the period of 27th June – 2nd July 2016. He/she will be permitted to attend the course, if selected.

Place: Name & Signature
Date: of the Sponsoring Authority
(seal of the institution)

Address for Correspondence:

Dr. Bindiya T S/ Dr. Sudhish N George,
Asst. Professor
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About the Department:

The Department of Electronics and Communication Engineering offers one B. Tech degree programme in Electronics and Communication Engineering and four regular M .Tech programmes, viz., Electronic Design Technology, Microelectronics & VLSI, Signal Processing and Telecommunication. The research programme leading to Ph.D. Degree in the broad areas of Electronics & Communication Engineering is also offered. Sponsored research programmes funded by various agencies are undertaken by the faculty of the department. For details see our website: www.ece.nitc.ac.in.

About the Institute:

National Institute of Technology Calicut (NITC), is fully centrally funded by MHRD and is governed by the NIT Act 2007. Institute has ten departments, three schools and nine research centers. It offers ten UG, and thirty PG programmes along with the Ph.D programme in various fields of Science, Technology and Engineering. Faculties in the various Departments have active collaborations with universities and elite institutions within and outside India for research and have active consultancy for industries. Website: www.nitc.ac.in.

Calicut City:

Calicut, also known as Kozhikode, is a blooming city in the region of Malabar, lying in the northern part of Kerala. This region is a major knowledge hub of Kerala and it proudly hosts many institutions of national eminence like NITC, IIMK, NIELIT, CWRDM, Kerala School of Mathematics, IISR etc. Calicut is well connected by rail/road/air to major cities in India. Apart from the serene beaches on the west and the high ranges of the Western Ghats on the east, there are many landmark places that attract attention of the tourists. NITC is 22km off the city limits towards east.

TEQIP Sponsored

Faculty Development Programme on

Advanced Digital Signal Processing Algorithms and Architectures

27th June – 2nd July, 2016

Coordinators

Dr. Bindiya T S

Dr. Sudhish N George

Organized by



Department of Electronics & Communication Engineering
National Institute of Technology Calicut
NIT Campus P.O., Kozhikode – 673601

Preamble:

The field of Digital Signal Processing (DSP) has seen explosive growth during the past few decades, as phenomenal advances both in research and applications have been made. DSP is used in numerous applications such as data compression, digital set-top box, portable video systems/computers, digital audio, multimedia and wireless communications, etc.. Advances in VLSI technologies opened-up promising methodologies to design, develop and implement a variety of signal processing algorithms for real world applications. This helped to mobilize the growth in DSP applications through fast and efficient implementations of DSP systems.

This course on Digital Signal Processing Algorithms and Architectures looks in depth at fundamental principles and algorithms in DSP together with system design and implementation considerations. Various topics will be handled by experts who have long term experience in the relevant field. A broad outline of the course is:

Major Topics:

- DSP Algorithms & Architectures
- Adaptive Filter Design
- Multirate Systems and Filter Banks
- VLSI Signal Processing
- Multimedia Signal Processing
- Wavelets: Theory & Construction
- FPGAs in DSP Applications
- Pattern Classification & Analysis
- Sparse Signal Processing

Resource persons:

Sessions will be handled by faculty experts from NITC and invited experts from Academic/Research Institutions.

Eligibility:

Course is open to faculty from NITs, AICTE approved engineering colleges/institutions and working professionals from Industries/R&D with basic degree in relevant field of Electrical Engineering. Limited number of seats will be given free of charge to selected faculty of AICTE Recognized engineering colleges, to promote them in teaching and research.

Registration:

TEQIP funded institutions:	Rs. 10,000/-
Industry/R&D:	Rs. 15,000/-
Selected faculty from AICTE Recognized Engineering Colleges & participants from NIT Calicut:	NIL

For provisional registration, send the duly filled up registration form as per the format to the Coordinator so as to reach on or before **20th June 2016**. **Number of participants will be restricted to thirty. Early intimation on participation through e-mail is highly appreciated.** Fees shall be payable when the selection is intimated. DD shall be drawn in favor of "Director, NIT Calicut" payable at SBI NIT Calicut (Code: 2207). Registration Form can be downloaded from NITC website.

Accommodation:

Lodging and Boarding will be provided with moderate facilities in the NITC campus for all selected participants.

Travel Expenses:

Payment of TA/DA from NITC will not be admissible for participants.

TEQIP-II

Faculty Development Programme on

Advanced Digital Signal Processing Algorithms and Architectures

27th June – 2nd July, 2016

REGISTRATION FORM

1. Name:
2. Date of birth: Gender (M/F): ...
3. Designation:
4. Department:
5. Institution:
6. Address for communication:
.....
.....
PinPhone No:
Mobile:
e-mail:
7. Highest Qualification:
8. Specialization:
9. Experience (Number of years):
Teaching: Industry:
10. Whether accommodation needed (Yes/No).....
11. DD No: and date: